

Electronic Acknowledgement Receipt

EFS ID:	2771832
Application Number:	10807648
International Application Number:	
Confirmation Number:	3901
Title of Invention:	Cached memory system and cache controller for embedded digital signal processor
First Named Inventor/Applicant Name:	Gilbert Christopher Sih
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Miscellaneous Incoming Letter	030459_01-25-08_Fee_Trans.pdf	451897	no	2
			5ed801ed967952502a3a37c4c78ec00a23742b442		

Warnings:

Information:

2	Fee Worksheet (PTO-06)	fee-info.pdf	30061	no	2
			7693300a49c92b3c0e0d1c1d0b99802273610c09		

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